



## UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
UNITED STATES PATENT AND TRADEMARK OFFICE  
WASHINGTON, D.C. 20231  
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 2773

<b>SERIAL NUMBER</b> 09/878,681	<b>FILING DATE</b> 06/11/2001 <b>RULE</b>	<b>CLASS</b> 257	<b>GROUP ART UNIT</b> 2811	<b>ATTORNEY DOCKET NO.</b> FIS9-1997-0163B-US4
<b>APPLICANTS</b> Ronald J. Bolam, E. Fairfield, VT; Subhash B. Kulkarni, Cortlandt Manor, NY; Dominic J. Schepis, Wappinger Falls, NY;				
<b>** CONTINUING DATA *****</b> THIS APPLICATION IS A CIP OF 09/859,146 05/16/2001 WHICH IS A CON OF 09/148,918 09/04/1998 PAT 6,281,095 WHICH IS A DIV OF 09/009,445 01/20/1998 PAT 6,133,610				
<b>** FOREIGN APPLICATIONS *****</b>				
<b>IF REQUIRED, FOREIGN FILING LICENSE GRANTED</b> <b>** 07/18/2001</b>				
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met Allowance <input checked="" type="checkbox"/>		<b>STATE OR COUNTRY</b> VT	<b>SHEETS DRAWING</b> 15	<b>TOTAL CLAIMS</b> 38
Verified and Acknowledged Examiner's Signature _____ Initials _____		<b>INDEPENDENT CLAIMS</b> 7		
<b>ADDRESS</b> Ratner & Prestia One Westlakes, Berwyn, Suite 301 P.O. Box 980 Valley Forge, PA 19482-0980				
<b>TITLE</b> Silicon-on-insulator chip having an isolation barrier for reliability				
<b>FILING FEE RECEIVED</b> 1484	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	